



## Statement of Volatility – Dell EMC PowerEdge R740 and R740XD

The Statement of Volatility provides you the information related to volatile and non-volatile components of different configurations of Dell EMC PowerEdge servers. Volatile components lose their data when power cord is removed from the system, whereas, non-volatile components continue to retain their data when the power has been removed from the component.

The following table provides information of different configurations of the PowerEdge R740 and R740XD servers.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
Planar										

PCH Internal CMOS RAM	Non-Volatile	1	U_PCH	256 Bytes	Battery backed CMOS RAM	No	Real-time clock and BIOS configuration settings	BIOS	N/A – BIOS only control	Perform the following steps:  1) Set NVRAM_CLR jumper to clear BIOS configuration settings at boot and reboot system;  2) AC power off system, remove coin cell battery for 30 seconds, replace battery and power on;  3) Restore default configuration in F2 system setup menu.
-----------------------	--------------	---	-------	-----------	-------------------------	----	---	------	-------------------------	--

06 - 2017

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
------	--------------------------	----------	----------------------	----------------	--	---	---------------------------------------	-------------------------------------	--------------------------------	------------------------

BIOS Password (part of PCH internal CMOS RAM)	Non-Volatile	1	U_PCH	16 Bytes (out of 256 bytes used for PCH Internal CMOS RAM)	Battery backed CMOS RAM	No	Password to change BIOS settings	Keyboard	N/A – BIOS only control	<p>1) Place shunt on J_PSWD_NVRAM jumper pins 2 and 4.</p> <p>2) AC power off is required after placing the shunt.</p> <p>3) AC power on with the shunt in place and then can be removed</p>
BIOS SPI Flash	Non-Volatile	1	U_PRIM_SPI_BIOS	32 MB	SPI Flash	No	Boot code	SPI interface via PCH	Software write protected	<p>You cannot remove the memory with any utilities or applications.</p> <p><b>Note:</b> When memory is corrupted or removed, System becomes non-functional</p>

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
------	--------------------------	----------	----------------------	----------------	--	---	---------------------------------------	-------------------------------------	--------------------------------	------------------------

iDRAC SPI Flash	Non-Volatile	1	U_IDRAC_SPI	4 MB	SPI Flash	No	iDRAC Uboot (bootloader)	SPI interface via iDRAC	Embedded iDRAC subsystem firmware actively controls sub area based write protection as needed.	User cannot clear the memory completely. However, user data, lifecycle log and archive, SEL, firmware image repository can be cleared via Delete Configuration and Retire System, accessible in Lifecycle Controller interface
BMC EMMC	Non-Volatile	1	U_eMMC	4 GB	eMMC NAND Flash	No	Operational iDRAC FW, Lifecycle Controller (LC) USC partition, LC service diags, LC OS drivers, USC firmware	NAND Flash interface via iDRAC	Embedded FW write protected	User cannot clear the memory completely. However, user data, lifecycle log and archive, SEL, firmware image repository can be cleared via Delete Configuration and Retire System, accessible in Lifecycle Controller interface
CPU Vcore and Memory regulator	Non-Volatile	2	U8003, U8043	16 KB	ROM	No	Operational parameters	Programmed at factory via I2C	Not write protected	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
Vmem Regulators	Non-Volatile	2	U8011, U8051	16 KB	ROM	No	Operational parameters	Programmed at factory via I2C	Not write protected	User cannot clear the memory.
System CPLD RAM	Volatile	1	U_CPLD	92 Kb	RAM	No	Not utilized	Not utilized	Not accessible	Not accessible
System CPLD FLASH	Non-Volatile	1	U_CPLD	256 Kb	Flash	No	Power on System Firmware	Firmware update	BIOS Security Protocols	User cannot clear the memory.
System Memory: RDIMM and LRDIMM	Volatile	Up to 12 per CPU	CPU1<2:1>_CH<5:0>_D<1:0>	Up to 32 GB per DIMM	DRAM	Yes	System OS RAM	System OS	OS Control	Reboot or power down the system.
System Memory: NVDIMM-N	Non-Volatile	Up to 6 per CPUs 1 and 2 (12 total in system)	CPU1<2:1>_CH<5:0>_D1	16 GB per NVDIMM-N	Flash – NVDIMM	No	Data integrity	When system initiates a Save (AC loss, shutdown, etc.), NVDIMM-N controller will transfer data from DRAM to Flash	Neither system nor OS can access the flash, only a system initiated Save will trigger the NVDIMM-N controller to transfer data from DRAM to flash	Using BIOS menu option, select NVDIMM factory reset.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
Internal USB Key	Non-Volatile	Up to 1	J_USB_INT	Varies (not factory installed)	Flash	Yes	General purpose USB key drive	USB interface via PCH. Accessed via system OS	Not write protected	Can be cleared in system OS
CPU	Volatile	1 or 2	CPU1 / CPU2	Various	Cache + registers	Yes	Processor cache + registers	Various	Various	Remove A/C
iDRAC DDR	Volatile	1	U_IDRAC9_DRAM1	512 Mb	DRAM	No	iDRAC local memory	iDRAC Firmware	Not write protected	Remove A/C
iDRAC	Volatile	1	U_IDRAC	For CPU: 128 KB + Registers  Co-proc: 64 Kb + Registers	Cache + registers	No	Processor cache + registers	iDRAC Firmware	Not write protected	Remove A/C

PIROM	Non-Volatile	1 or 2	CPU1 / CPU2	256 Bytes	EEPROM	No	Processor info + scratchpad	SMBus interface to iDRAC	Out of 256 bytes, 128 bytes are protected by Intel. The other 128 bytes are not write protected.	User cannot clear the memory.
-------	--------------	--------	-------------	-----------	--------	----	-----------------------------	--------------------------	--	-------------------------------

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
Recovery BIOS SPI	Non-Volatile	1	U208	16 MB	SPI Flash	No	Recovery image	SPI interface via iDRAC	Not write protected.	User cannot clear the memory.
<b>2x3.5" Rear Backplane</b>										
SEP internal flash	Non-Volatile	1	U_SEP	Flash: 64 KB+4 KB EEPROM: 2 KB	Integrated Flash+EEPROM	No	Firmware + FRU	I2C interface via iDRAC	Program write protected.	User cannot clear the memory.
Backplane External FRU	Non-Volatile	1	U_BP_EEPROM	256 Bytes	I2C EEPROM	No	FRU	Programmed at ICT during production.	Not write protected.	User cannot clear the memory.

4x3.5" Mid Backplane										
SEP internal flash	Non-Volatile	1	U_SEP	Flash: 64 KB+4 KB EEPROM: 2 KB	Integrated Flash+EEPROM	No	Firmware + FRU	I2C interface via iDRAC	Program write protected.	User cannot clear the memory.
Backplane External FRU	Non-Volatile	1	U_BP_EEPROM	256 Bytes	I2C EEPROM	No	FRU	Programmed at ICT during production.	Not write protected.	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
24x2.5" EXP/Backplane										
NVSRAM Memory	Non-Volatile	1	U_NVSRAM	1 Mb	Flash	No	FW config data	Common Flash memory Interface (CFI)	Hardware strapping	User cannot clear the memory.
Flash Memory	Non-Volatile	1	U_FLASH	128 Mb	Flash	No	Firmware	Common Flash memory Interface (CFI)	Hardware strapping	User cannot clear the memory.



Expander FRU	Non-Volatile	1	U_EXP_EEPROM	512 Bytes	I2C EEPROM	No	FRU	I2C interface via expander	Hardware strapping	User cannot clear the memory.
Backplane FRU	Non-Volatile	1	U_BP_EEPROM	256 Bytes	I2C EEPROM	No	FRU	I2C interface via iDRAC	Hardware strapping	User cannot clear the memory.
<b>16x2.5" EXP/Backplane</b>										
NVSRAM Memory	Non-Volatile	1	U_NVSRAM	1 Mb	Flash	No	FW config data	Common Flash memory Interface (CFI)	Hardware strapping	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
Flash Memory	Non-Volatile	1	U_FLASH	128 Mb	Flash	No	Firmware	Common Flash memory Interface (CFI)	Hardware strapping	User cannot clear the memory.
Expander FRU	Non-Volatile	1	U_EXP_EEPROM	512 Bytes	I2C EEPROM	No	FRU	I2C interface via Expander	Hardware strapping	User cannot clear the memory.
Backplane FRU	Non-Volatile	1	U_BP_EEPROM	256 Bytes	I2C EEPROM	No	FRU	I2C interface via iDRAC	Hardware strapping	User cannot clear the memory.

8x2.5" Backplane										
SEP internal flash	Non-Volatile	1	U_SEP	Flash: 32 KB+4 KB  EEPROM: 2 KB	Integrated Flash+EEPROM	No	Firmware + FRU	I2C interface via iDRAC	Program write protected.	User cannot clear the memory.
8x3.5" Backplane										
SEP internal flash	Non-Volatile	1	U_SEP	Flash: 64 KB+4 KB  EEPROM: 2 KB	Integrated Flash+EEPROM	No	Firmware + FRU	I2C interface via iDRAC	Program write protected.	User cannot clear the memory.
Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
12x3.5" EXP/Backplane										
NVSRAM Memory	Non-Volatile	1	U_NVSRAM	1 Mb	Flash	No	FW config data	Common Flash memory Interface (CFI)	Hardware strapping	User cannot clear the memory.

Flash Memory	Non-Volatile	1	U_FLASH	128 Mb	Flash	No	Firmware	Common Flash memory Interface (CFI)	Hardware strapping	User cannot clear the memory.
Expander FRU	Non-Volatile	1	U_EXP_EEPROM	512 Bytes	I2C EEPROM	No	FRU	I2C interface via expander	Hardware strapping	User cannot clear the memory.
Backplane FRU	Non-Volatile	1	U_BP_EEPROM	256 Bytes	I2C EEPROM	No	FRU	I2C interface via iDRAC	Hardware strapping	User cannot clear the memory.
4x2.5" Rear Backplane										
SEP internal flash	Non-Volatile	1	U_SEP	Flash: 64 KB+4 KB EEPROM: 2 KB	Integrated Flash+EEPROM	No	Firmware + FRU	I2C interface via iDRAC	Program write protected.	User cannot clear the memory.
Backplane External FRU	Non-Volatile	1	U_BP_EEPROM	256 Bytes	I2C EEPROM	No	FRU	Programmed at ICT during production.	Not write protected.	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
------	--------------------------	----------	----------------------	----------------	--	---	---------------------------------------	-------------------------------------	--------------------------------	------------------------

**24x2.5" NVMe**

SEP internal flash	Non-Volatile	2	U_SEP1, U_SEP2	Flash:64KB +4KB EEPROM: 2KB	Integrated Flash+EEPROM	No	Firmware + FRU	I2C interface via iDRAC	Program write protect bit	User cannot clear the memory.
Hotplug Detect IC	Non-Volatile	2	U35, U42	Flash: 16KB	FRAM	No	Firmware	I2C interface via iDRAC	Program write protect bit	User cannot clear the memory.
Microcontroller	Non-Volatile	2	U8	Flash: 128KB + 16KB	Integrated Flash+SRAM	No	Firmware (Cypress framework + plx payload)	Cypress framework is offline program, PLX payload can be updated via iDRAC I2C	Program write protect bit	User cannot clear the memory.
PCIe Switch	Volatile	2	PLX1, PLX2	Flash: 2KB payload size	Integrated Flash	no	Programmed at boot via Cypress IC	I2C	Program write protect bit	User cannot clear the memory when under operation. Memory is auto cleared after a DC cycle
Expander FRU image	Non-Volatile	1	U_FRU	512 Bytes	I2C EEPROM	No	FRU	I2C interface via expander	Hardware strapping	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
H740, H740P, H830 PERC										
NVSRAM	Non-Volatile	1	U1087	128 KB	NVSRAM	No	Configuration data	ROC writes configuration data to NVSRAM	Not write protected. Not visible to Host Processor	User cannot clear the memory.
FRU	Non-Volatile	1	U1019	256 B	FRU	No	Card manufacturing information	Programmed at ICT during production.	Not write protected.	User cannot clear the memory.
SPD	Non-Volatile	1	U22	256 B	SPD	No	Memory configuration data	Preprogrammed before assembly	Not write protected. Not visible to Host Processor	User cannot clear the memory.
Flash	Non-Volatile	1	U1086	16 MB	Flash	No	Card firmware	Preprogrammed before assembly. Can be updated using Dell/LSI tools	Not write protected. Not visible to Host Processor	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
Backup Flash	Non-Volatile	1	U1100	8 GB	Backup Flash	No	Holds cache data during power loss	FPGA backs up DDR data to this device in case of a power failure	Not write protected. Not visible to Host Processor	Flash can be cleared by powering up the card and allowing the controller to flush the contents to VD's. If the VD's are no longer available, cache can be cleared by going into controller bios and selecting Discard Preserved Cache.
SDRAM	Volatile	9	U1077U1085	8 GB	SDRAM	No	Cache for HDD I/O	ROC writes to this memory - using it as cache for data IO to HDDs	Not write protected. Not visible to Host Processor	Cache can be cleared by powering off the card
H330 PERC										
NVSRAM	Non-Volatile	1	U1033	128 KB	NVSRAM	No	Configuration data	ROC writes configuration data to NVSRAM	Not write protected. Not visible to Host Processor	User cannot clear the memory.



NVSRAM	Non-Volatile	1	U1033	128 KB	NVSRAM	No	Configuration data	ROC writes configuration data to NVSRAM	Not write protected. Not visible to Host Processor	User cannot clear the memory.
FRU	Non-Volatile	1	U1019	256 B	FRU	No	Card manufacturing information	Programmed at ICT during production	Not write protected.	User cannot clear the memory.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
SBR	Non-Volatile	1	U1020	8 KB	Serial Boot ROM	No	Bootloader	Preprogrammed before assembly	Not write protected. Not visible to Host Processor	User cannot clear the memory.
Flash	Non-Volatile	1	U3	16 MB	Flash	No	Card firmware	Preprogrammed before assembly. Can be updated using Dell/LSI tools	Not write protected. Not visible to Host Processor	User cannot clear the memory.

**PCIe SSD Extender Card**





Microcontroller	Non-Volatile	1	USAM7	32 Mb	SPI Flash	No	For field maintenance. Have License, Service Tag and system information. Driving health and status LEDs	SPI interface via iDRAC	Hardware strapping	User cannot clear the memory.
-----------------	--------------	---	-------	-------	-----------	----	---	-------------------------	--------------------	-------------------------------

## TPM

Trusted Platform Module (TPM)	Non-Volatile	1	U_TPM	128 Bytes	EEPROM	Yes	Storage of encryption keys	Using TPM Enabled operating systems	Software write protected	F2 Setup option
-------------------------------	--------------	---	-------	-----------	--------	-----	----------------------------	-------------------------------------	--------------------------	-----------------

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
------	--------------------------	----------	----------------------	----------------	--	---	---------------------------------------	-------------------------------------	--------------------------------	------------------------

### Right Control Panel

SPI Flash	Non-Volatile	1	U2	32 Mb	SPI Flash	No	Easy Restore functionality: contains Service Tag, Copy of SEL logs	SPI interface from iDRAC to Right Cntl Panel	Embedded iDRAC subsystem firmware actively controls sub area based write protection as needed.	User cannot clear the memory.
-----------	--------------	---	----	-------	-----------	----	--	--	--	-------------------------------

#### IDSDM - vFlash

vFlash (uSD)	Non-Volatile	1	J3	16 GB	NAND flash	yes	populate outof-band or optionally connect to the host as mass storage and boot mechanism	User can provide data to iDRAC (entirely in the iDRAC domain) to be pushed into vFlash	Not write protected.	<p>1. Card may be physically removed and destroyed or cleared via standard means, on a separate computer.</p> <p>OR</p> <p>2. User has access to the card in the host domain and can clear it manually</p>
--------------	--------------	---	----	-------	------------	-----	--	--	----------------------	--


Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write protected?	How is memory cleared?
------	--------------------------	----------	----------------------	----------------	--	---	---------------------------------------	-------------------------------------	--------------------------------	------------------------

iDSDM (uSD1, uSD2)	Non-Volatile	2	J1, J2	16 GB, 32 GB, 64 GB	NAND Flash	Yes	Provides mass storage	device resides in host domain; they are exposed to the user via an internally connected, non removable USB mass storage device	Physical write protected switch on ACE card	<p>1. Card may be physically removed and destroyed or cleared via standard means, on a separate computer.</p> <p>OR</p> <p>2. User has access to the card in the host domain and can clear it manually</p>
SPI Flash	Non-Volatile	2	U2	1 MB	SPI Flash	SPI flash is only indirectly connected to iDRAC. iDRAC can read any address in the SPI flash, but may only write the primary firmware storage area as a part of a firmware update procedure.	Boot firmware storage, configuration and state data for IDSDM.	User can initiate a firmware update of the IDSDM device.	There is no mechanism provided to iDRAC to write any SPI NOR area outside of the primary IDSDM firmware region.	iDRAC may issue a clear command to erase all contents of the SPI NOR. This action may leave the IDSDM non-functional.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How memory is write protected?	How is memory cleared?
------	--------------------------	----------	----------------------	----------------	--	---	---------------------------------------	-------------------------------------	--------------------------------	------------------------



Microcontroller	Non-Volatile	1	IC1	256 KB	Internal Flash	No	Bootloader and software implementation of LCD command set	Updated as part of secure iDRAC software update. Configuration parameters can change only as part of iDRAC update	Writes are only allowed as part of secure iDRAC update	User cannot clear the memory.
PSU										
Microcontroller	Non-Volatile	Up to 3	Microchip	Up to 64 KB	Flash PROM and EEPROM	Yes	Report PSU information and control firmware	The data is flash via Dell Update Package (DUP)	Using signature and manufacture key to write protect the memory.	The memory is cleared before firmware update
Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How memory is write protected?	How is memory cleared?
Network Card (Mellanox ConnectX 3)										

Serial EEPROM	Non-Volatile	1	U30	32Kb	EEPROM	No	Contains Vital Product Data	Write is done only in the factory with special tools	Physical Jumper on the card is needed to write	User cannot clear the memory.
SPI Serial Flash	Non-Volatile	1	U39	16Mb	SPI Flash	No	Configuration data, FW	In the factory field with FW update tools	Write are only allowed with FW update tools.	N/A
 <b>NOTE:</b> For any information that you may need, direct your questions to your Dell Marketing contact.										

Copyright © 2019 Dell Inc. or its subsidiaries. All Rights Reserved.

Dell, EMC, and other trademarks are trademarks of Dell Inc. or its subsidiaries. Other trademarks may be trademarks of their respective owners.